DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

(Under 37 CFR § 1.63; includes reference to PCT International Applications)

As the below named inventors, we hereby declare that:

Our residence, post office address and citizenship are as stated below next to our names.

We believe we are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention ENTITLED:

SEMICONDUCTOR MEMORY DEVICE WITH SURFACE STRAP AND METHOD OF FABRICATING THE SAME

the specification	n of which:		
	is attached hereto was filed on as: United States Application Serial No as a National Phase or Continuation of PCT Application No, filed _ and designating the U.S., and published with amendments through (if a	r Continuation-in-Part or Divisio	nal of
	reby state that we have reviewed and un including the claims, as amended by any		ve-identified
	cnowledge the duty to disclose to the Unbe material to patentability as defined it		
application(s) for one country other the country other the	beby claim foreign priority benefits under for patent or inventor's certificate or of a fer than the United State of America list patent or inventor's certificate or any Ponan the United States of America filed the fine application(s) on which priority is claim	any PCT International application ted below and have also identifie CT International applications despoy us on the same subject matter	n(s) designating at least and below any foreign signating at least one
Prior Foreign/P	CT Application(s) [list additional appli	cations on separate page]:	Priority Claimed:
Country (or Japan	PCT) Application Number: 2003-166851	Filed (Day/Month/Year) June 11, 2003	Yes No
We here	eby claim the benefit under 35 U.S.C. {	§119(e) of any United States app	lication listed below:
(Applic	eation Number) (Filing Date)		
	eby claim the benefit under Title 35, University of the PCT international application(s) design	•	

prior application(s) in the manner provided by the first paragraph of Title 35, United States Code § 112, we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to us

below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those

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to be material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. (or U.S.-designating PCT) Application(s) [list additional applications on separate page]:

U.S. Serial No.: Filed (Day/Month/Year) PCT Application No. Status (patented, pending, abandoned)

We hereby appoint Edgar H. Haug, Registration No. 29,309, and Grace L. Pan, Registration No. 39,440, and Frommer Lawrence & Haug LLP, or their duly appointed associate, our attorneys, with full power of substitution and revocation, to prosecute this application, to make alterations and amendments therein, to file continuation and divisional applications thereof, to receive the Patent, and to transact all business in the Patent and Trademark Office and in the Courts in connection therewith, and to insert the Serial Number of the application in the space provided above, and specify that all communications about the application are to be directed to the following correspondence address:

Grace L. Pan, Esq. c/o FROMMER LAWRENCE & HAUG LLP 745 Fifth Avenue New York, NY 10151

INVENTORS:

Direct all telephone calls to: (212) 588-0800

to the attention of: Grace L. Pan

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

arrans.			
Signature:		Date:	
Full name of first joint inventor:	Masaru Kito		
Residence:	Yokohama-shi, Japan		
Citizenship:	Japan		
Signature:		Date:	
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